

Master Thesis in Analog Design

Location: Infineon Villach

Job ID: 10633



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Master Thesis Proposal

Motivation

State of the art power drivers/switches provide diagnostic functions with the necessity of accurate current sensing.

Currently the most used sensing technologies (shunt & sense FET) are at the edge of fulfilling the new application requirements.

Target

Development of an accurate current sensing solution for integrated low-side drivers/switches:

- **Accuracy: +/- 5% over temperature and process corners**
- **Current Range: 100 mA – 10 A**

Strategy

- Refine the application requirements.
- Analyze the state of the art solutions.
- Develop a good understanding of the BCD technology features/limitations and choose the best sensing concept.
- Design the circuit concept and schematic of an integrated current sensing solution for a low side switch in the target technology SPT9U.
- Perform a lab characterization of the prototype (optional, based on test chip schedule).

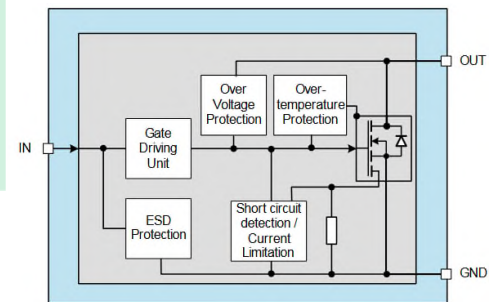
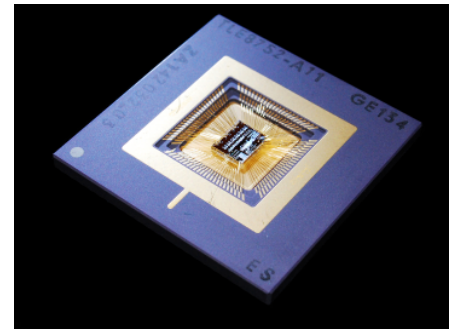
Terms & Conditions

Start: 10/16 (min. 6 months)

Working time: full-time or part-time (min. 20 hrs/ week, by arrangement)

This position is subject to the collective agreement for workers and employees in the electrical and electronics industry. The salary for this position is at least €1.311 gross p.m (full-time basis).

Profile requirements for this position are available at the following link:
<http://www.infineon.com/cms/en/careers/jobsearch/jobsearch/10633-Industriepraktikum-Masterarbeit-Analog-Design/>



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